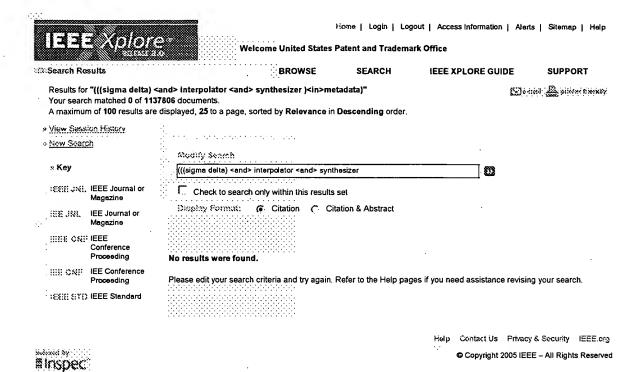
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				AbstractPlus Full Text: PDF (568	3 KB)	Heee CNF			
				One bit digital filtering					
		1	٦.	Angus, J.A.S.; Digital Filters: An Enabling Tech	nolog	y (Ref. No. 1998	/252), IEE Colloquium o	n	
				20 April 1998 Page(s):8/1 - 8/6		4400 4547			
				AbstractPlus Full Text: PDF(43)	o KB)	HER UNF			
		Ē	4.	Issues in the design of low ov Hossack, D.M.; Reid, J.; Sewell, Oversampling Techniques and S 30 Mar 1994 Page(s):3/1 - 3/5	J.I.;			odulators	
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		Γ	5.	Design and implementation of Horbach, U.; Lang, M.; Circuits and Systems, 1991., IEE	E Inte	ernational Sympo		ed loop fil	ters
				11-14 June 1991 Page(s):1633 -					
		34040		AbstractPlus Full Text: PDF(27)	2 ND)	mad con			
			6.	Use of noise shaping for rate of Ryan, I.; McCarthy, O.; Circuits and Systems, 2003. ISC Volume 4, 25-28 May 2003 Page	AS '0:	3. Proceedings of	of the 2003 International	Symposiu	m on
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		T.	7.	A four-channel digital signal perving four speech channels Haspeslagh, D., Sevenhans, J., Solid-State Circuits, IEEE Journal	in a r Delari al of	new-generation bre, A.; Kiss, L.;	subscriber line circui) conversion
				Volume 26, Issue 7, July 1991 F AbstractFlus Full Text: PDF (58					
					,				
		٢	8.	High performance signal acquicurtis, T.E.; Webb, A.B.; Civil Applications of Sonar Syste 4 Feb 1991 Page(s):2/1 - 2/5					
				AbstractPlus Full Text: PDF(21	2 KB)	iee Cnf			
			9.	A CMOS oversampling D/A co Su, D.K.; Wooley, B.A.; Solid-State Circuits, IEEE Journa	al of			econstruc	tion filter
				Volume 28, Issue 12, Dec. 1993 <u>AbstractPlus</u> Full Text: <u>PDF</u> (88			1		

10. Stable single-bit noise-shaping quantizer based on data coding into optimized binary vectors Γ. Yagyu, M.; Nishihara, A.; Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on Volume 2, 26-29 May 2002 Page(s):Il-384 - Il-387 vol.2 AbstractPlus | Full Text: PDF (502 KB) - IEEE CNF 11. A CMOS oversampling D/A converter with a current-mode semi-digital reconstruction filter Su, D.K., Wooley, B.A.; Solid-State Circuits Conference, 1993. Digest of Technical Papers. 40th ISSCC., 1993 IEEE International 24-26 Feb. 1993 Page(s):230 - 231 AbstractPlus | Full Text: PDF(172 KB) ISSE CRE Γ. 12. A novel interpolator architecture for ΣΔ DACs Francesconi, F.; Lazzari, G.; Liberali, V.; Maloberti, F.; Torelli, G.; Design Automation, 1993, with the European Event in ASIC Design. Proceedings. [4th] European Conference on 22-25 Feb. 1993 Page(s):249 - 253 AbstractPlus | Full Text: PDF(248 KB) | ISSE CAS

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